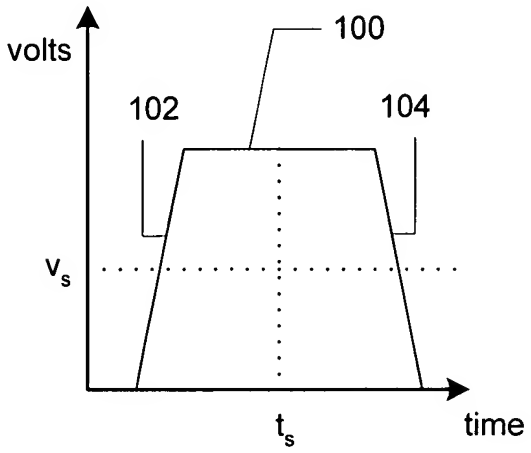

FIG. 1A

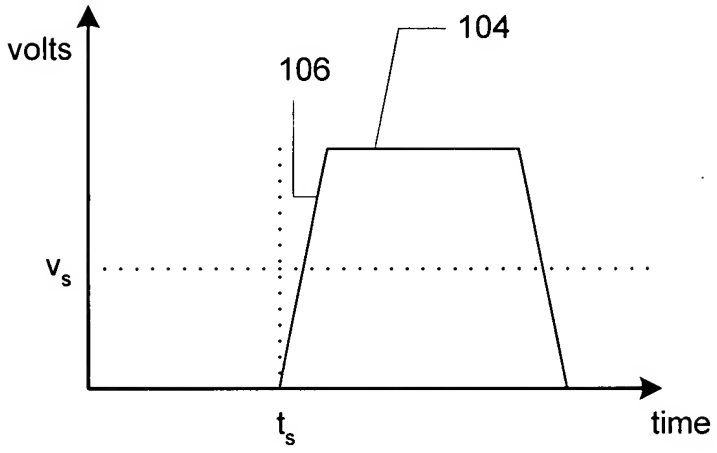

FIG. 1B

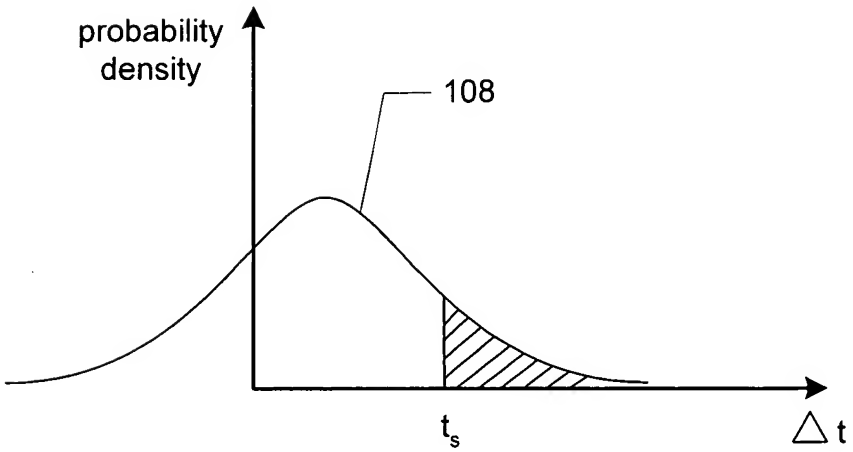

FIG. 1C

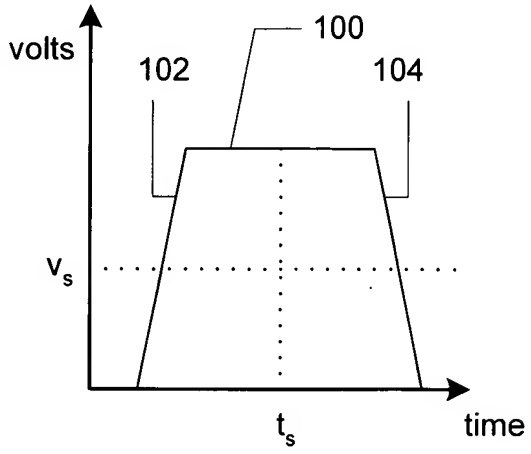

FIG. 2A

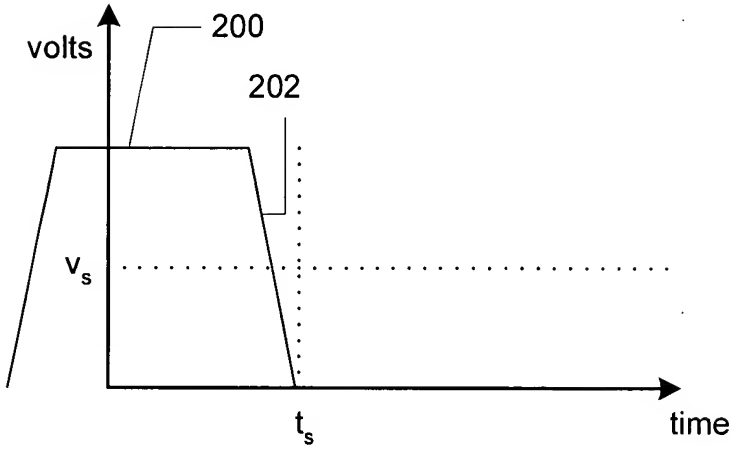

FIG. 2B

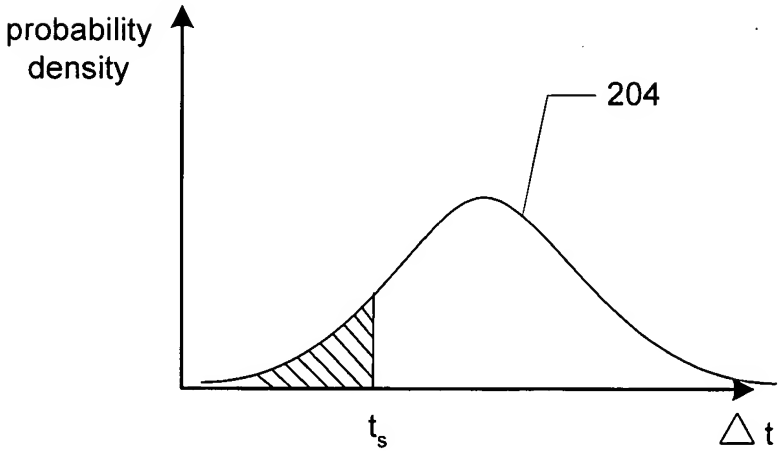

FIG. 2C

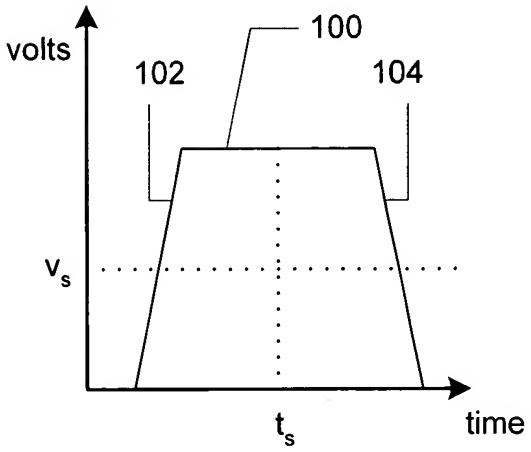

FIG. 3A

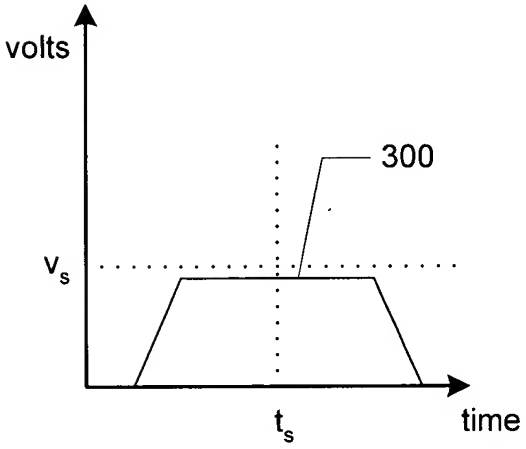

FIG. 3B

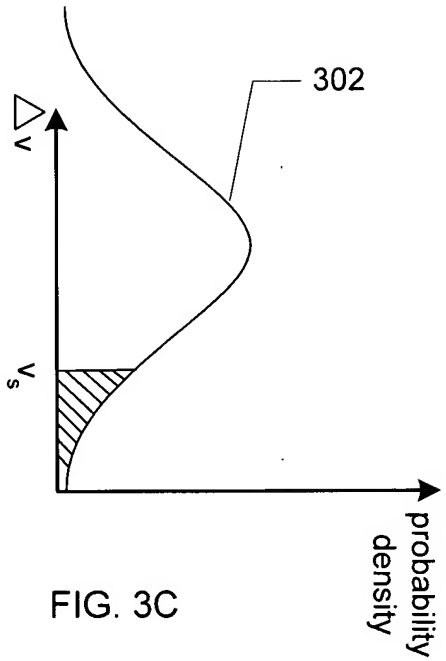

FIG. 3C

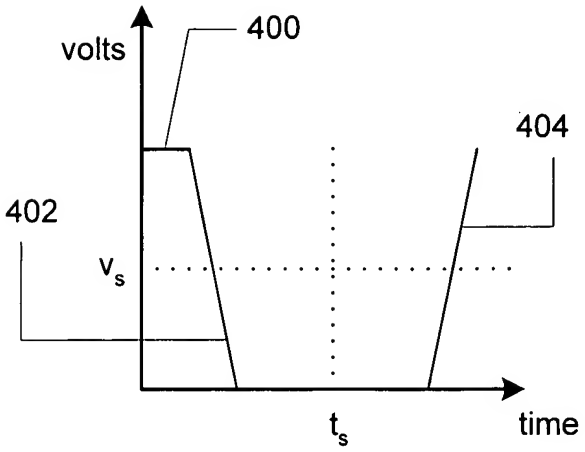

FIG. 4A

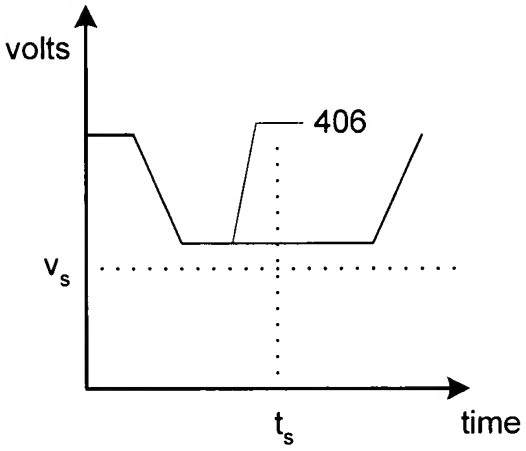

FIG. 4B

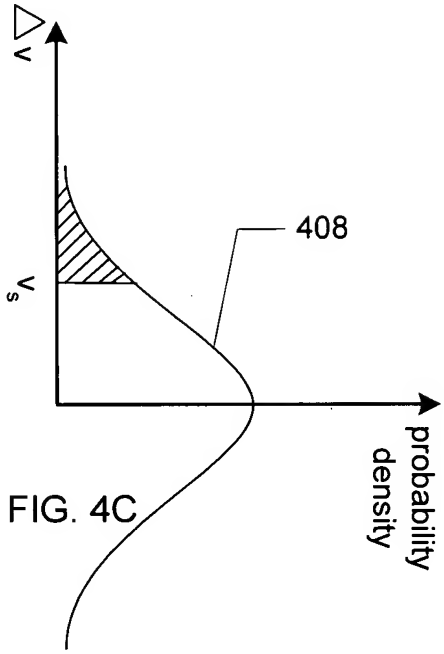

FIG. 4C

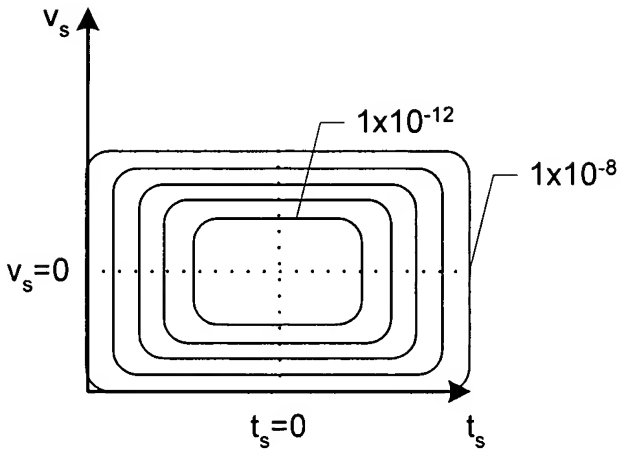

FIG. 5

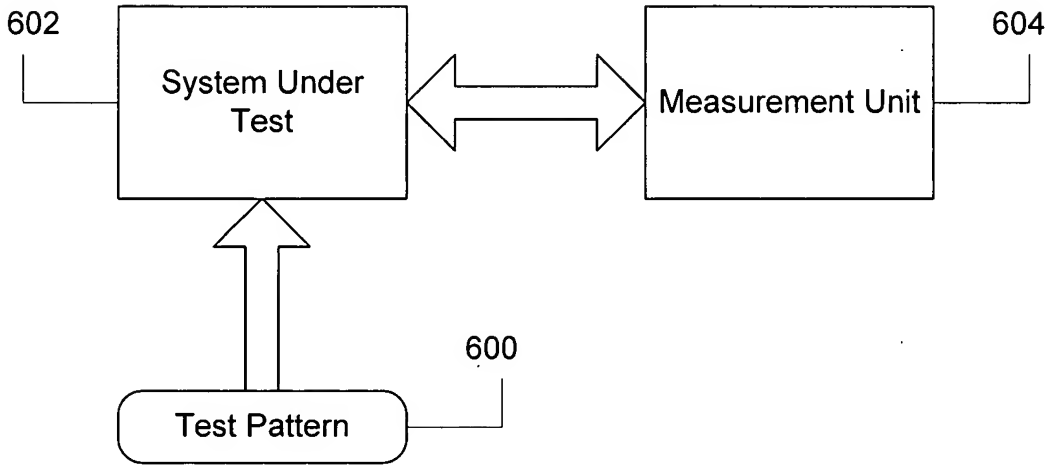
602
System Under Test
Measurement Unit
604
600
Test Pattern

FIG. 6

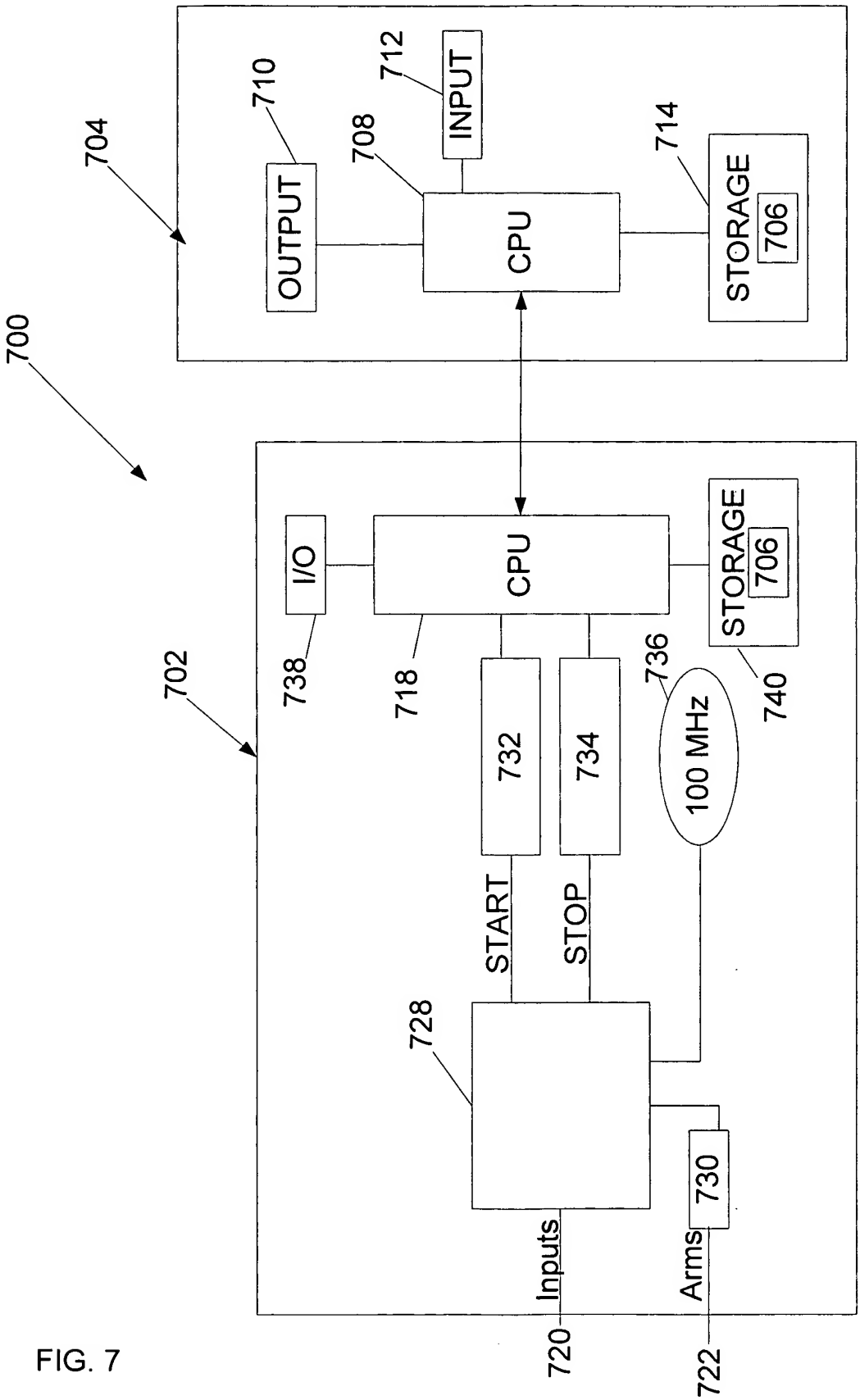
700
704
710
OUTPUT
712
INPUT
708
CPU
714
STORAGE
706
702
I/O
CPU
STORAGE
706
738
718
732
734
736
100 MHz
740
START
STOP
728
730
Inputs
Arms
720
722

FIG. 7